

FIG. 1

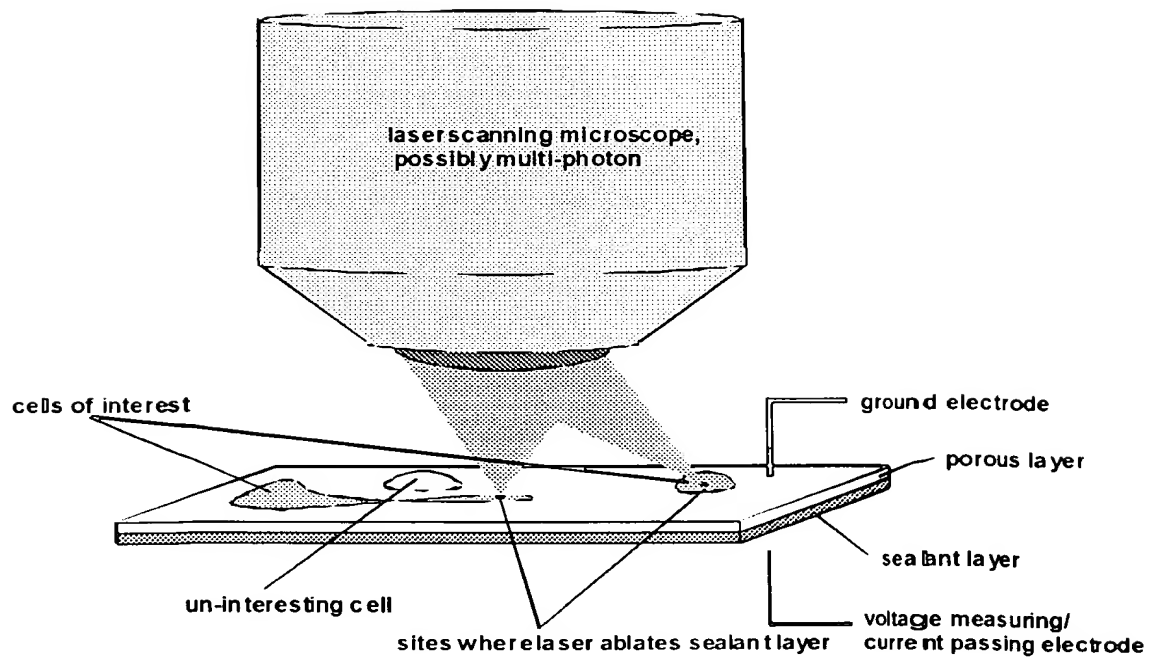


FIG. 2

Wafer/Chip

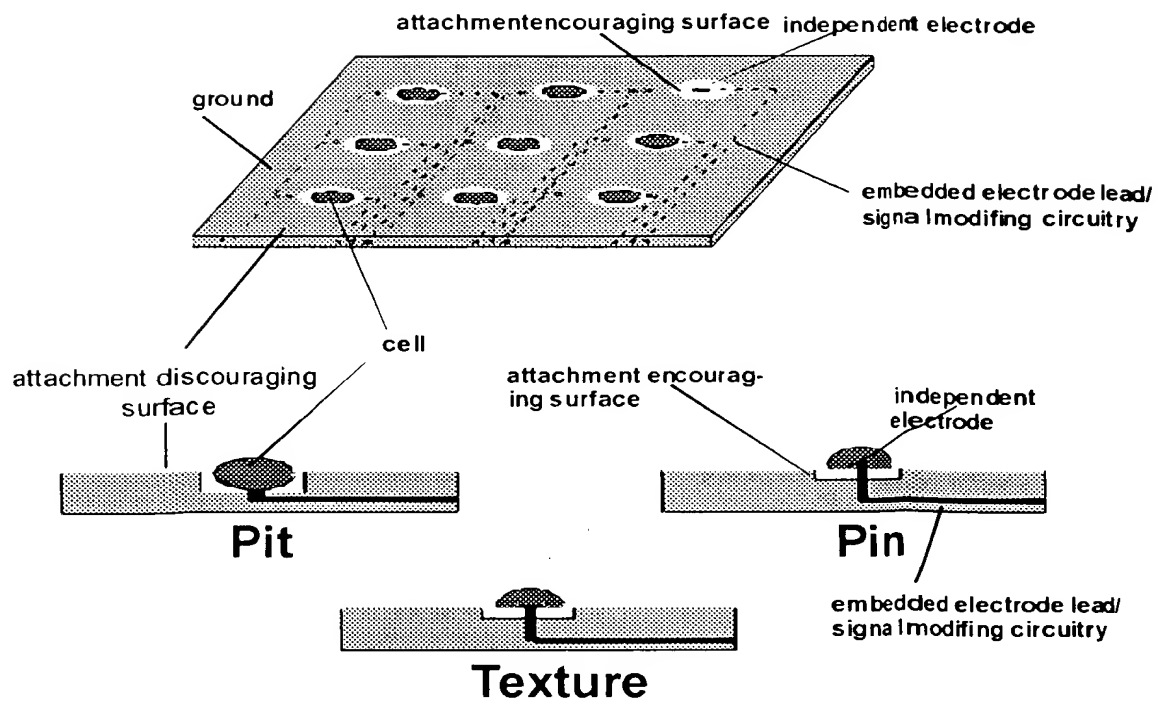


FIG. 3